

#7B 924/02 Docket No. 20661-801D1

In re Application of:
Varun Singh, et al.

Serial No.: 09/964,192

Filed: 9/26/01

) Examiner: Eugene Lee
)

Group Art Unit: 2815

For: AN IMPROVED POLYSILICON RESISTOR HAVING ADJUSTABLE TEMPERATURE COEFFICIENTS AND THE METHOD OF MAKING THE SAME

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

BOX: NON-FEE AMENDMENT Assistant Commissioner for Patents Washington, D.C. 20231 CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited postage paid with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231

on 9-11-02

Signature Julie Tipton

Dear Sir:

AMENDMENT

Responsive to the Office Action dated June 19, 2002, please amend the above-identified application for patent as follows:

IN THE CLAIMS

Please amend the claims as follows:

RECHNOLOGY CENTER 2800